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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/726,924	12/03/2003	Kazuhiro Itoh	5267-74	4695
27799	7590 01/21/2005		EXAMINER	
COHEN, PONTANI, LIEBERMAN & PAVANE			HAM, SEUNGSOOK	
551 FIFTH A SUITE 1210			ART UNIT	PAPER NUMBER
NEW YORK, NY 10176			2817	
			DATE MAILED: 01/21/2003	5

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
055 - 1 - 4 - 4 0	10/726,924	ITOH ET AL.				
Office Action Summary	Examiner	Art Unit				
	Seungsook Ham	2817				
- The MAILING DATE of this communication app Period for Reply	pears on the cover sheet with the c	orrespondence address				
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a repl If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a reply be timely within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE.	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. \$ 133).				
Status						
1)⊠ Responsive to communication(s) filed on 02 A	pril 2004.					
,	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4)⊠ Claim(s) <u>1-4</u> is/are pending in the application.						
	4a) Of the above claim(s) is/are withdrawn from consideration.					
5) Claim(s) is/are allowed.						
6) Claim(s) 1-4 is/are rejected.						
7) Claim(s) is/are objected to.	•					
8) Claim(s) are subject to restriction and/c	or election requirement.					
Application Papers						
9) The specification is objected to by the Examine	ar -					
10)⊠ The drawing(s) filed on <u>03 December 2003</u> is/are: a)⊠ accepted or b)□ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.33(a).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
	n priority updor 35 H S.C. & 110(a)	(d) or (f)				
12)⊠ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a)⊠ All b)□ Some * c)□ None of:						
1. Certified copies of the priority documents have been received.						
 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this National Stage 						
	·	d in this National Stage				
application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.						
	or the definied copies flot receive	u.				
Attack						
Attachment(s)	n□	(070, 440)				
Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948)	4) ∐ Interview Summary Paper No(s)/Mail Da					
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) 5) Notice of Informal Patent Application (PTO-152)						
Paper No(s)/Mail Date <u>4/2/04.</u> 6)						

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DETAILED ACTION

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-4 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kashima et al. (US '611) in view of Igarashi (US '798).

Kashima et al. (figs. 1(a), 1(b)) discloses a coupling structure for a waveguide to a microstrip line comprising: a mode conversion region defined in a printed circuit board 8, the region being surrounded by a first conductive film 11 formed on a front surface of the board, a second conductive film 38 formed on a back surface of the board, and a conductive wall 12 connecting the respective peripheries of the first and second conductive films and formed of a plurality of through-holes filled with conductive material; the first conductive film being connected to a microstrip line 9 on the board; a first slot (i.e., the slot in the middle of the copper foil 11) formed in the front surface of the region; a second slot 7 formed in a surface of the waveguide; and the first and second slots are adapted to be disposed in opposed relation to one another.

Kashima et al. does not show the waveguide being a dielectric waveguide resonator. It should be noted that the waveguide of Kashima et al. resonates at a certain frequency dependent on the length of the waveguide.

Igarashi (fig. 3) discloses a dielectric waveguide resonator coupled to a microstrip line through a slot. Igarashi also discloses a conventional a hollow waveguide resonator (fig. 7) and teaches using a dielectric waveguide resonator instead of using a hollow waveguide resonator.

It would have been obvious to one of ordinary skill in the art to provide a dielectric waveguide resonator instead of the hollow waveguide in the device of Kashima et al. since hollow waveguide and dielectric waveguide are functionally equivalent as shown by Igarashi (see figs. 3 and 7).

Regarding claim 3, it is inherent from the device of Kashima et al. that the first and second slots achieve TE mode coupling since the shape of slots are identical to the applicant's claimed invention as well as both disclose a waveguide to microstrip line coupling.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Wu et al. (US Pat. App. Publ. '426) discloses a waveguide to laminated waveguide transition through a slot.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Seungsook Ham whose telephone number is (571) 272-2405. The examiner can normally be reached on Monday-Thursday, 8:00AM-5:00PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (571)-272-1769. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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